

**Notice of References Cited**

Application/Control No.

09/908,941

Applicant(s)/Patent Under  
Reexamination  
HIRASE ET AL.

Examiner

Jennifer M. Kennedy

Art Unit

2812

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,968,610 A	10-1999	Liu et al.	427/579
	B	US-6,087,208 A	07-2000	Krivokapic et al.	438/183
	C	US-			
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	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, Volume 1- Process Technology, 1986, Lattice Press, page 1.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.